



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of

ITO

Application Number: 10/770,479

Filed: February 4, 2004

**For: INTEGRATED CIRCUIT, INTEGRATED
CIRCUIT DESIGN METHOD AND
HARDWARE DESCRIPTION GENERATION
METHOD TO GENERATE HARDWARE
BEHAVIOR DESCRIPTION OF INTEGRATED
CIRCUIT**

Attorney Docket No. NITT.0189

**Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231**

Art Unit 2825

**Examiner
Thompson, Annette M.**

PRELIMINARY AMENDMENT

Sir:

This is a Preliminary Amendment accompanying a Request for Continuing Examination and is in response to the Final Office Action dated May 18, 2005 in the above identified application, the period for response to which is set to expire on October 18, 2005, with a two-month extension fee filed herewith. Please amend the above identified application as follows: